

UTILITY PATENT APPLICATION  
UNDER 37 CFR 1.53(b)

11002 U.S. PTO  
09/867766  
05/31/01

Box PATENT APPLICATION  
Commissioner for Patents  
Washington, DC 20231

Case Docket No. 60188-075

Sir:

Transmitted herewith for filing is the patent application of:

INVENTOR: Makoto FUJIWARA  
FOR: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF TESTING  
SEMICONDUCTOR INTEGRATED CIRCUIT

Enclosed are:

- ☒ 15 pages of specification, claims, abstract.
- ☐ Declaration and Power of Attorney.
- ☒ Priority Claimed Japanese Patent Application No. 2000-161343
- ☐ Certified copy of \_\_\_\_\_
- ☒ 4 sheets of formal drawing.
- ☐ An assignment of the invention to \_\_\_\_\_  
and the assignment recordation fee.
- ☐ An associate power of attorney.
- ☒ Information Disclosure Statement, Form PTO-1449 and reference.
- ☒ Return Receipt Postcard
- ☐

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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